

Working Group 10.5 (Design and Engineering of Electronic Systems)
Yearly report for the period August 2025 – September 2026
Written by Graziano Pravadelli

The WG 10.5 home page is located at: <https://vlsi-soc.github.io/ifip-wg10.5/>

1 - Members

One member retired and he asked to be removed from the WG:

- Prof. Prof. **Takashi Kambe**, JKinki University, Japan

Then, there are in total 56 members and 12 honorary members.

The geographic distribution of the members is as follows:

- Europe: 37 members, 4 honorary members
- North America: 7 members, 5 honorary members
- Latin America: 3 members, 1 honorary member
- Asia and South Pacific 6 members, 2 honorary members
- Africa and Middle Est: 3 members

2 - Officers

Since October 2020 the WG chair is Prof. Graziano Pravadelli, University of Verona, Italy

Since March 2021 the WG vice chairs are:

- Prof. Ibrahim Elfadel, Masdar Institute, United Arab Emirates
- Prof. Ricardo Reis, Universidade Federal do Rio Grande do Sul, Brazil
- Dr. Victor Grimblatt, Synopsys, Chile
- Prof. Tiziana Margaria, University of Limerick, Ireland

The roles of the WG chairs are as follows:

Name	Geographical area	Main expertise	Promotional role
Tiziana Margaria	Europe	Formal methods	Women enrolment
Ibrahim Elfadel	Africa, Asia and Far East	Cyber physical systems and IoT	Mentoring for VLSI-SOC organizers
Ricardo Reis	America	Physical design and testing	Networking with IFIP initiatives
Victor Grimblatt	Worldwide	EDA, electronic industry	Industrial liaison

3 - Meetings

The following business meetings took place in the period August 2025 to July 2026:

- October 12, 2025, hybrid, co-located with VLSI-SOC 25 in Puerto Varas (Chile), 9 attendees.
- April 21, 2026, hybrid, co-located with DATE 26 in Verona (Italy), 19 attendees.

All meeting minutes are available at: <https://vlsi-soc.github.io/ifip-wg10.5/meetings.html>

The following meetings are currently planned for the coming year:

- October 11, physical meeting in Limassol (Cyprus) during VLSI-SOC 2026, with possibility of remotely connect for people that will not attend the conference.

4 - Sponsored Events

The main conference sponsorship activity of WG 10.5 is the annual IFIP/IEEE International Conference on Very Large Scale Integration, VLSI-SOC Conference.

The members of the Steering Committee are:

Luis Miguel Silveira (INESC ID, PT), Chi-Ying Tsui (HKUST, Hong Kong, CN), Manfred Glesner (TU Darmstadt, DE), Ibrahim Elfadel (Masdar Institute of Science and Technology, UAE), Fatih Ugurdag (Ozyegin University, TR), Ricardo Reis (UFRGS, BR), Ian O'Connor (ECL, FR), Masahiro Fujita (The University of Tokyo, JP), Salvador Mir (TIMA, FR), Graziano Pravadelli (University of Verona, IT)

VLSI-SoC 2025

The 33rd VLSI-SoC Conference was held in person in Puerto Varas, Chile, from October 12 to 15, 2025. The conference was co-sponsored by IFIP, IEEE CEDA, IEEE CASS, Synopsys, Chipus, Rydev, and Universidad de La Frontera.

The conference was organized and chaired by Victor Grimblatt, Synopsys, Chile, and Pierre-Emmanuel Gaillardon, University of Utah, USA. The Program Co-Chairs were Prof. Ricardo Reis, UFRGS, Brazil, and Prof. Patrick Groeneveld, Stanford University, USA.

The technical program included 3 keynote presentations, 6 regular sessions, 2 special sessions, 2 embedded tutorials, 1 panel session, 1 industrial session, and 1 PhD Forum.

The conference received 44 valid submissions, of which 29 regular papers were accepted. In addition, 2 special sessions were organized, comprising a total of 7 papers.

The participation and involvement of WG members were limited. As a consequence of the lower-than-expected attendance, the conference incurred a financial loss, and one of the main organizers, Dr. Victor Grimblatt, had to personally cover the resulting deficit.

This situation prompted a broader discussion within the present WG members regarding its current geographical imbalance. A significant proportion of WG members are based in Europe, while participation tends to decrease when the conference is held in locations far from Europe. This raises an important issue concerning the long-term sustainability of organizing VLSI-SoC conferences in geographically distant locations and, more generally, in emerging countries.

The WG is then invited to discuss the need to identify appropriate mechanisms to support the organization of conferences in such locations and to mitigate the financial risks faced by local organizers. Ensuring the geographical diversity of conference venues is important not only for the international character of VLSI-SoC, but also for providing researchers, students, and professionals in different regions with opportunities to participate in international scientific events without the substantial costs and barriers associated with long-distance travel. Without adequate support mechanisms, there is a risk that future conferences will become increasingly concentrated in regions where the majority of WG members are located, reducing opportunities for local scientific communities elsewhere to engage directly with the international VLSI-SoC community.

Events co-sponsored by WG 10.5 without financial commitment

- **SBCCI 2026** - 39th Symposium on Integrated Circuits and Systems Design, August 24-28, 2026, Sao Paulo, Brazil.
- **FDL 2026** – Forum on Specification and Design Languages, September 9-11, 2026, Rome, Italy. The general chair was Graziano Pravadelli.
- **2nd Advanced School on AI and Intelligent Systems (AIS)** for young professionals focused on topics relevant to the WG. The second edition was in Favignana Island, Italy from June 8 to 11, 2026. The school was sponsored by University of Verona, Synopsys, Cadence, IEEE CEDA and IEEE CASS. The following WG members contributed to the organization and the lectures: Graziano Pravadelli, Nicola Bombieri, Ian O'Connor, Giovanni De Micheli, Florenc Demrozi, Yuanqing Cheng.

Planned Events

The following main sponsored events are already planned:

- **IFIP/IEEE VLSI-SoC 2026:** Limassol, Cyprus, October 2026, General Chairs: Prof. Tiziana Margaria and Prof. Ian O'Connor.
- **IFIP/IEEE VLSI-SoC 2027:** Beijing, China, second week of October 2027, Organizers: Prof. Yuanqing Cheng

The following events are actually considered to be sponsored without any financial implication:

- **SBCCI 2027:** August, 2027
- **FLD 2027:** Paris, France, September, 2027
- **AIS School 2027:** Favignana, Italy, June 2027

5 – Other initiatives and discussions

Prof. Manfred Gleasner has been nominated for the IFIP fellow award. Results are expected in October from the IFIP General Assembly

During the WG 10.5 meeting that was held in Tangier in 2024, WG members discussed membership rules: after two years of inactivity (no participation in VLSI-SOC, no submission to VLSI-SOC, no participation to WG meetings, non involvement in other initiatives of the WG), the WG chair will contact members via email to assess their interest and availability to continue participating in the WG.

At the end of September 2026 the following 20 members appear to be inactive, according to the rule, and will be contacted by the chair to ask for their interest in remaining members: Josè Ayala, Ayse Coskun, Nikil Dutt, Dan Gale, Masaharu Imai, Ahmed Jerraya, Jing-Yang Jou, Odysseas Koufopavlou, Enrico Macii, Wolfgang Nebel, Hillel Ofek, Maciej Ogorzalek, Adam Pawlak, Franz J. Rammig, Michel Robert, Donatella Sciuto, Leandro Soares Indrusiak, Alexander Stempkovsky, Lionel Torres, Akihiko Yamada.

Then the geographic distribution of active WG members is as follows:

Active members	
North America	3
Latin America	3
Europe	24
Asia and South Pacific	3
Africa and Middle East	3
TOTAL	36